

REMARKS

Claims 1-5 are pending of which claim 1 is independent. In this Amendment, claim 1 has been amended. Care has been exercised not to introduce new matter.

Rejections of Claims Under 35 U.S.C. § 102

Claims 1-5 were rejected under 35 U.S.C. § 102(e) as being anticipated by Nose et al. (U.S. Patent No. 6,819,311, hereinafter “Nose”). The rejection is respectfully traversed for the following reasons.

Amended claim 1, *inter alia*, recites “switching said reference voltage is performed so that an image display period for supplying said image display voltage and a black display period for supplying the black display voltage are contained in one horizontal period, and the switching the reference voltage is synchronized with change in selection line signals of lines in which an image of said selection line is written and lines in which black is written regardless of data to be displayed.” As shown in FIG. 2, one example of what is claimed in claim 1, the image display voltage and the black display voltage are obtained by switching the reference voltage itself which is supplied from the reference voltage generator circuit 301. The switching of the reference voltage is synchronized with change of selection line signals 500 regardless of data to be displayed 404.

Nose fails to disclose the limitations of claim 1.

Turning to Nose, scanning(selection) line signals VG1 to VGn consists of an image data selection period t1 and a black display selection period t2 during which a gradation voltage corresponding to the image data is written on the pixel electrode 5 and the image data selection period t1 during which a voltage corresponding to a black display is written on the pixel electrode 5. The change of scanning line signals exactly corresponds to the change of data to be

displayed. Nose is silent on generation of the reference voltage supplied to the signal line drive IC. Even assuming that Nose generates reference voltages in synchronization with change of scanning line signals like what is claimed in claim 1, the generated reference voltages should be corresponding to image data to be displayed, because the change of scanning line signals exactly corresponds to the change of data to be displayed. In contrast, claim 1 requires “the switching the reference voltage” to be “synchronized with change in selection line signals...regardless of data to be displayed.”

Hence, Nose does not satisfy the claim requirements that the image write voltage and the black write voltage are generated by switching the reference voltage itself which is supplied from the reference voltage generator circuit and is inputted to the signal line driver IC, in synchronization with change of the selection line signals regardless of data to be displayed. As anticipation under 35 U.S.C. § 102 requires that each element of the claim in issue be found, either expressly described or under principles of inherency, in a single prior art reference, *Kalman v. Kimberly-Clark Corp.*, 713 F.2d 760, 218 USPQ 781 (Fed. Cir. 1983), based on the foregoing, it is submitted that Nose does not anticipate claim 1, nor any claim dependent thereon. Thus, claim 1 and claims dependent thereon are patentable over Nose.

Conclusion

Upon entry of the above claim amendments, claims 1-5 remain active in this application. Applicant submits that all of the claims are in condition for allowance. Accordingly, this case should now be ready to pass to issue; and Applicant respectfully requests a prompt favorable reconsideration of this matter.

To the extent necessary, a petition for an extension of time under 37 C.F.R. 1.136 is hereby made. Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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